

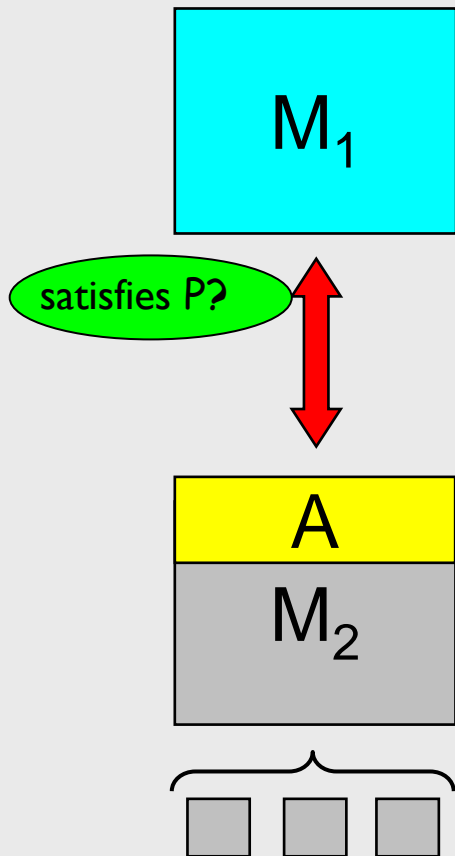


Compositional Verification

Dimitra Giannakopoulou and Corina Păsăreanu
CMU / NASA Ames Research Center

compositional verification

does system made up of M_1 and M_2 satisfy property P ?



- ▶ check P on entire system: too many states!
- ▶ use system's natural decomposition into components to break-up the verification task
- ▶ check components in isolation:

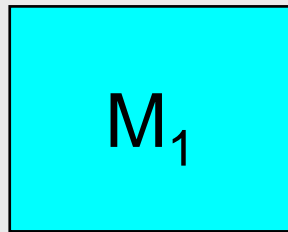
does M_1 satisfy P ?

- components typically satisfy requirements in specific contexts / environments
- ▶ assume-guarantee reasoning
 - introduces **assumption** A representing M_1 's "context"

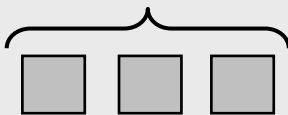
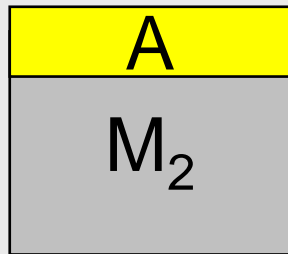
examples of assumptions

- ▶ will not invoke “close” on a file if “open” has not previously been invoked
- ▶ accesses to shared variable “X” must be protected by lock “L”
- ▶ (rover executive) whenever thread “A” reads variable “V”, no other thread can read “V” before thread “A” clears it first
- ▶ (spacecraft flight phases) a docking maneuver can only be invoked if the launch abort system has previously been jettisoned from the spacecraft

assume-guarantee reasoning



satisfies P?



reasons about triples:

$\langle A \rangle M \langle P \rangle$

is *true* if whenever M is part of a system that satisfies A , then the system must also guarantee P

simplest assume-guarantee rule (ASYM):

1. $\langle A \rangle M_1 \langle P \rangle$

2. $\langle true \rangle M_2 \langle A \rangle$

$\langle true \rangle M_1 \parallel M_2 \langle P \rangle$

“discharge” the assumption

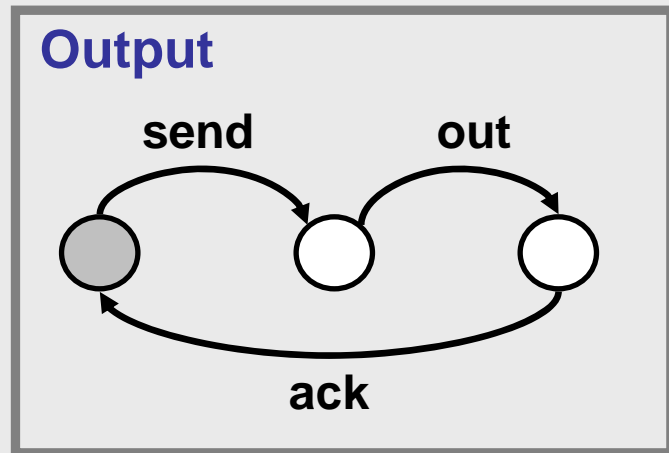
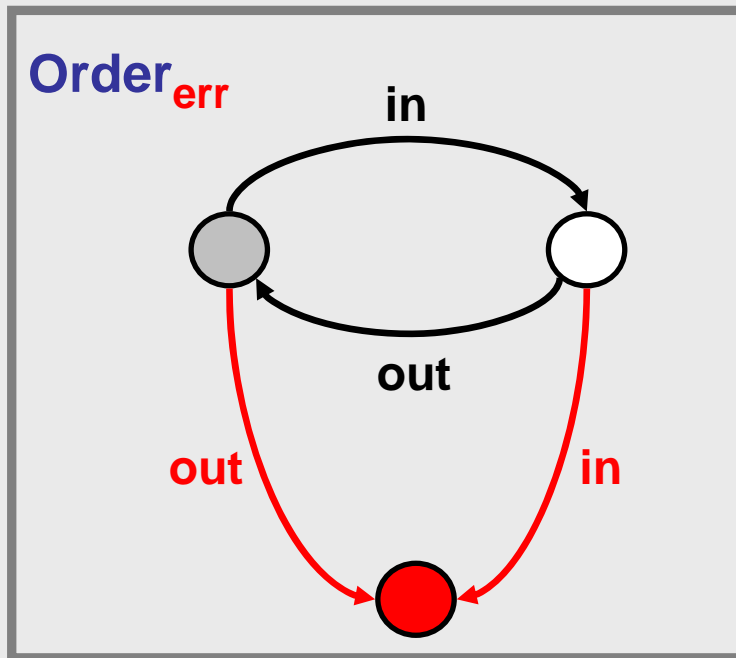
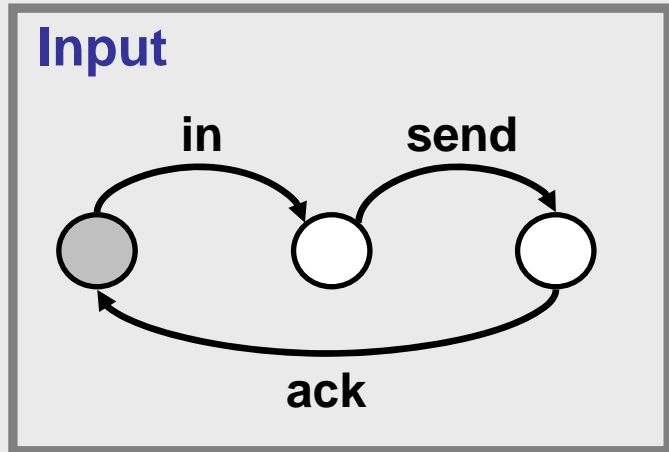
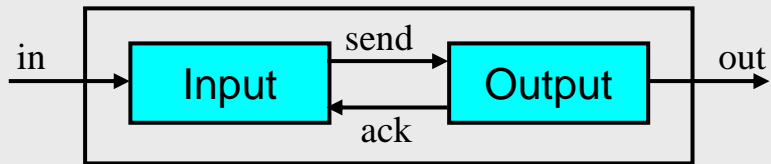
how do we come up
with the assumption?

formalisms

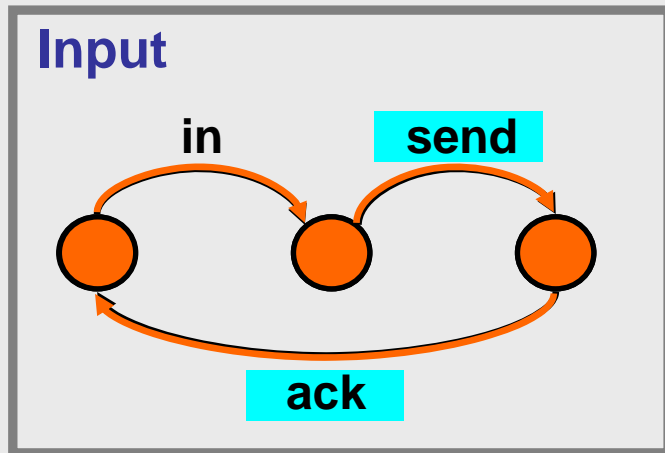
- ▶ components modeled as **finite state machines** (FSM)
 - FSMs assembled with parallel composition operator “||”
 - synchronizes shared actions, interleaves remaining actions
- ▶ a safety property P is a **FSM**
 - P describes all legal behaviors in terms of its alphabet
 - P_{err} – complement of P
 - determinize & complete P with an “**error**” state;
 - bad behaviors lead to error
 - component M satisfies P iff error state unreachable in $(M \parallel P_{\text{err}})$
- ▶ **assume-guarantee** reasoning
 - assumptions and guarantees are FSMs
 - $\langle A \rangle M \langle P \rangle$ holds iff error state unreachable in $(A \parallel M \parallel P_{\text{err}})$

example

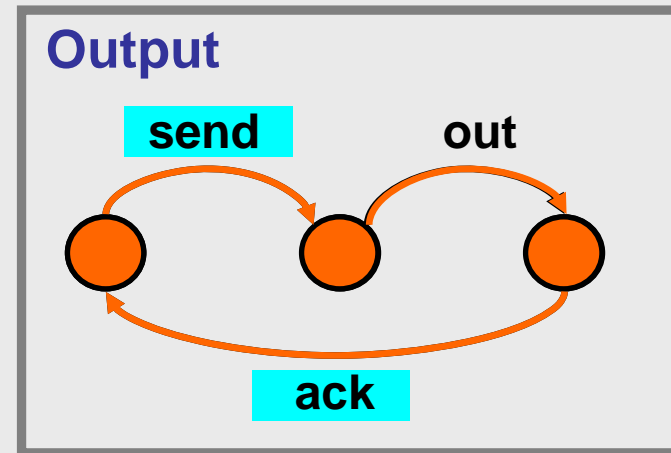
Require in and out to alternate (property Order)



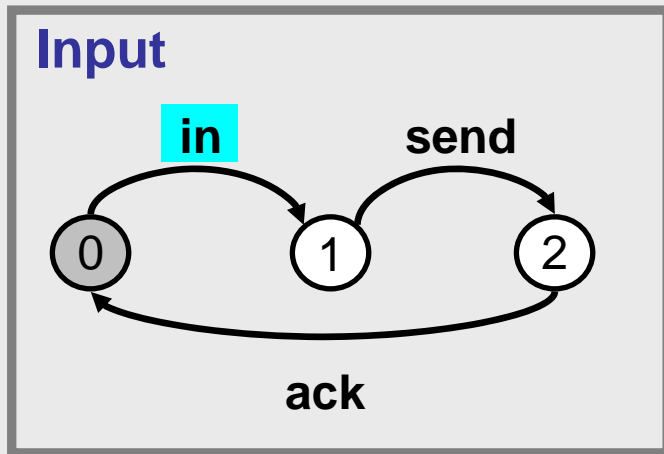
parallel composition



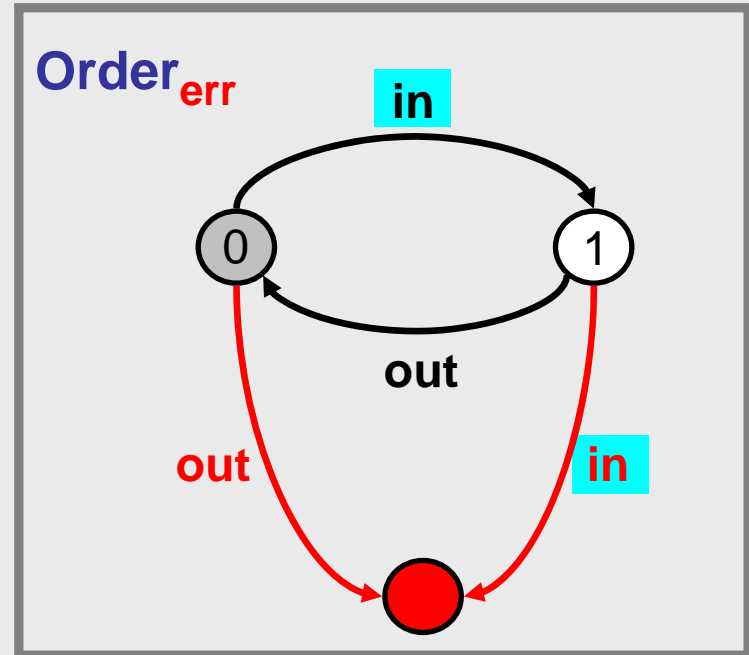
||



property satisfaction



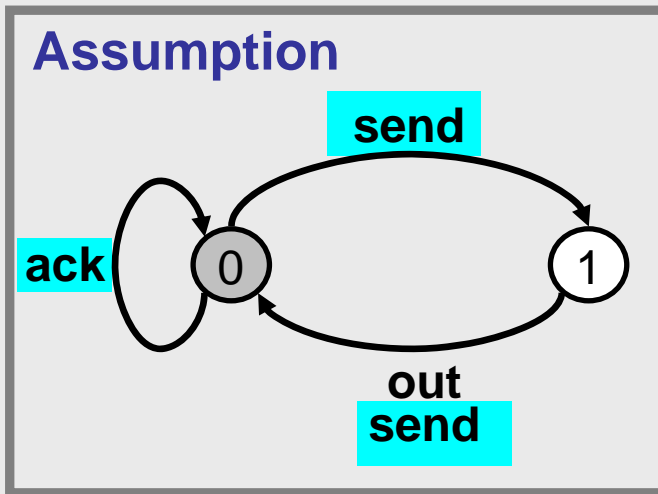
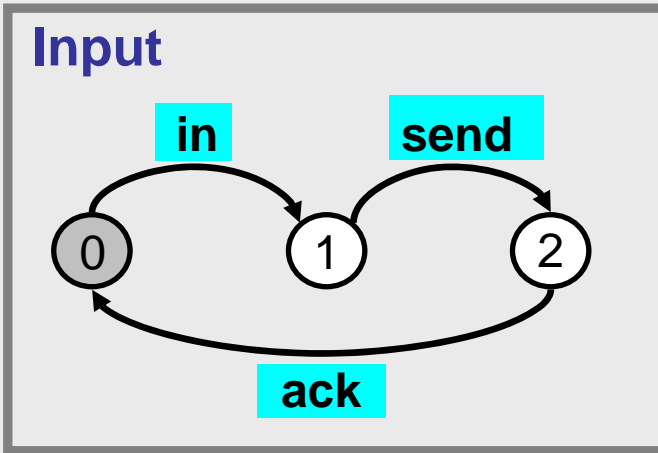
||



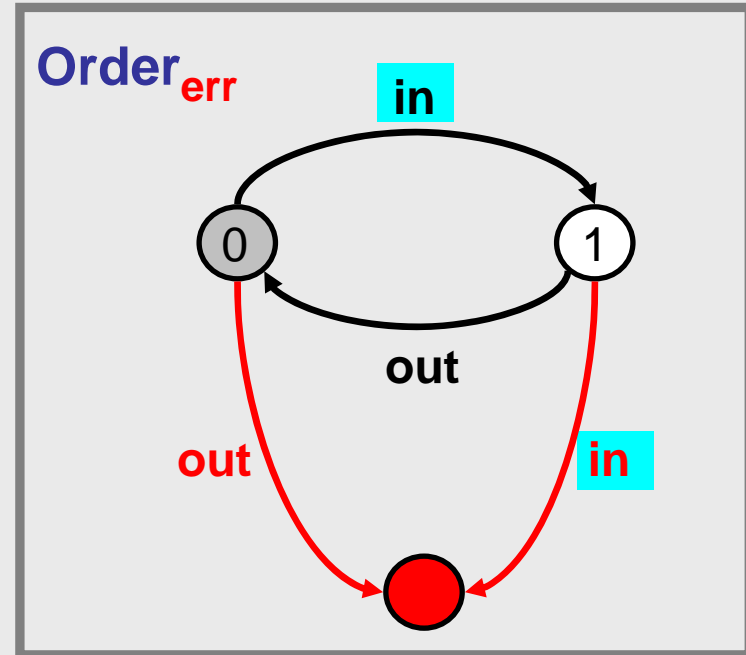
crex. 1: $(I_0, O_0) \text{ out } (I_0, O_{\text{error}})$

crex. 2: $(I_0, O_0) \text{ in } (I_1, O_1) \text{ send } (I_2, O_1) \text{ out } (I_2, O_0) \text{ out } (I_2, O_{\text{error}})$

assume-guarantee reasoning



||



crex 1: (I_0, A_0, O_0) out **X**

crex 2: (I_0, A_0, O_0) in (I_1, A_0, O_1) send (I_2, A_0, O_1) out **X**

the weakest assumption

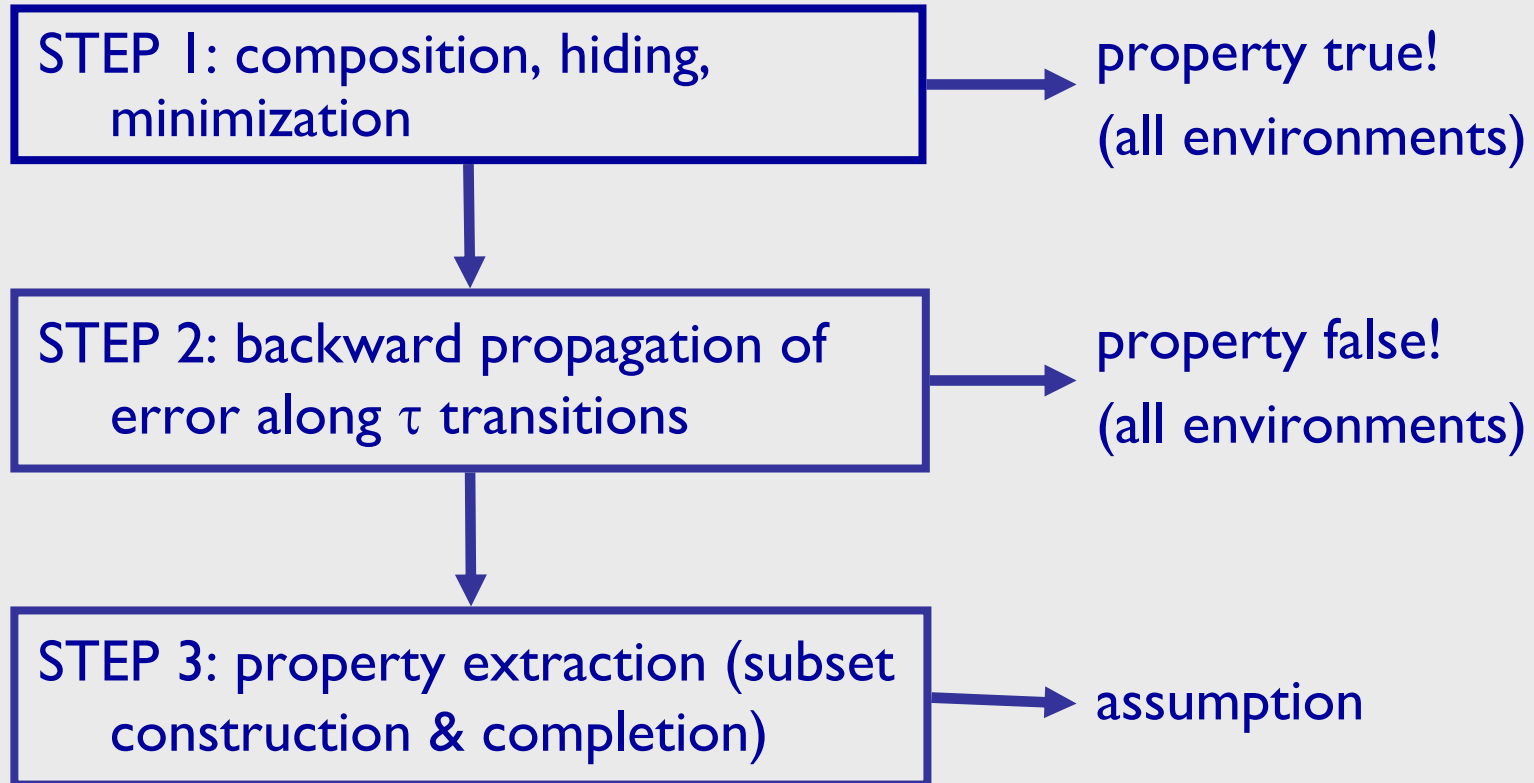
▶ given component M, property P, and the interface of M with its environment, generate the **weakest** environment assumption **WA** such that: $\langle \text{WA} \rangle M \langle P \rangle$ holds

▶ weakest means that for all environments E:

$$\langle \text{true} \rangle M \parallel E \langle P \rangle \text{ IFF } \langle \text{true} \rangle E \langle \text{WA} \rangle$$

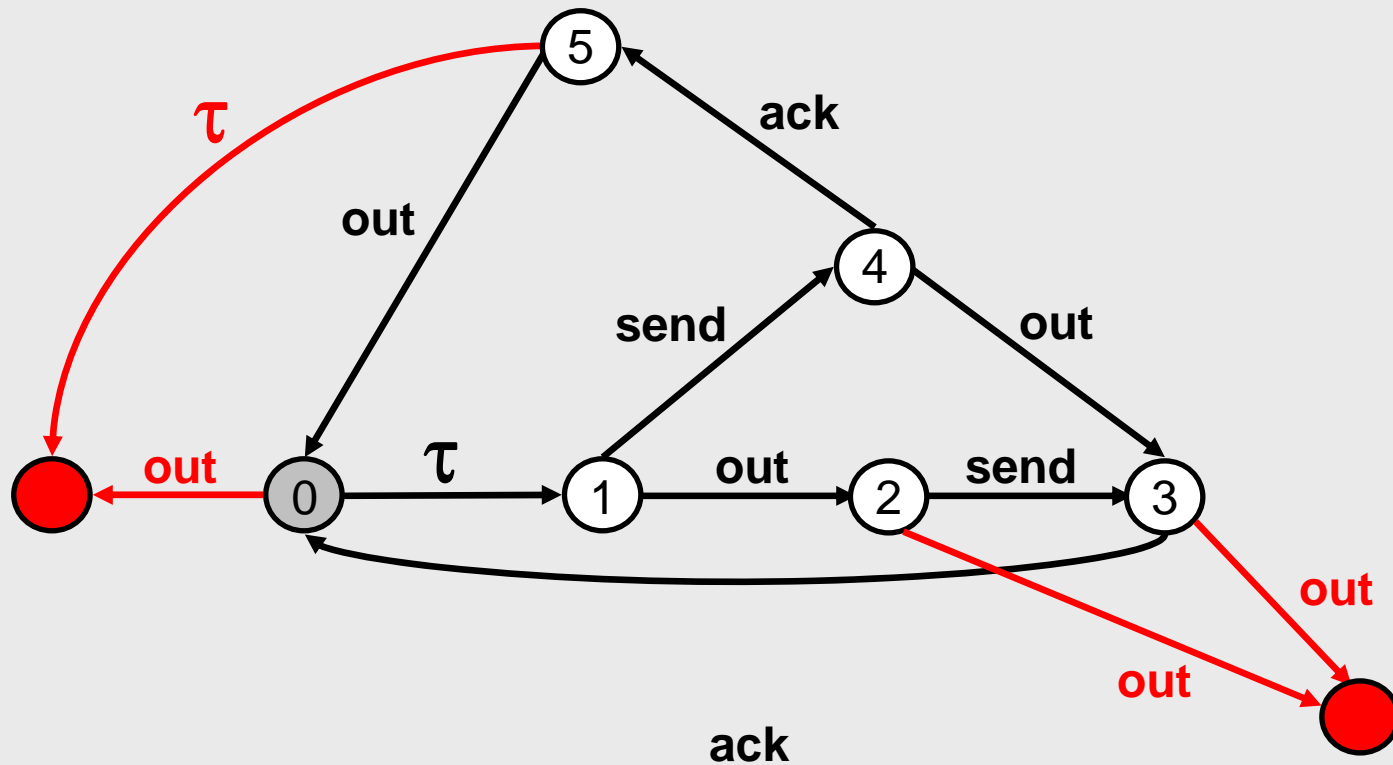
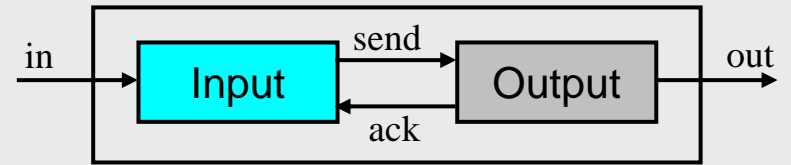
▶ in other words, weakest means **safe** and **permissive**

assumption generation [ASE'02]

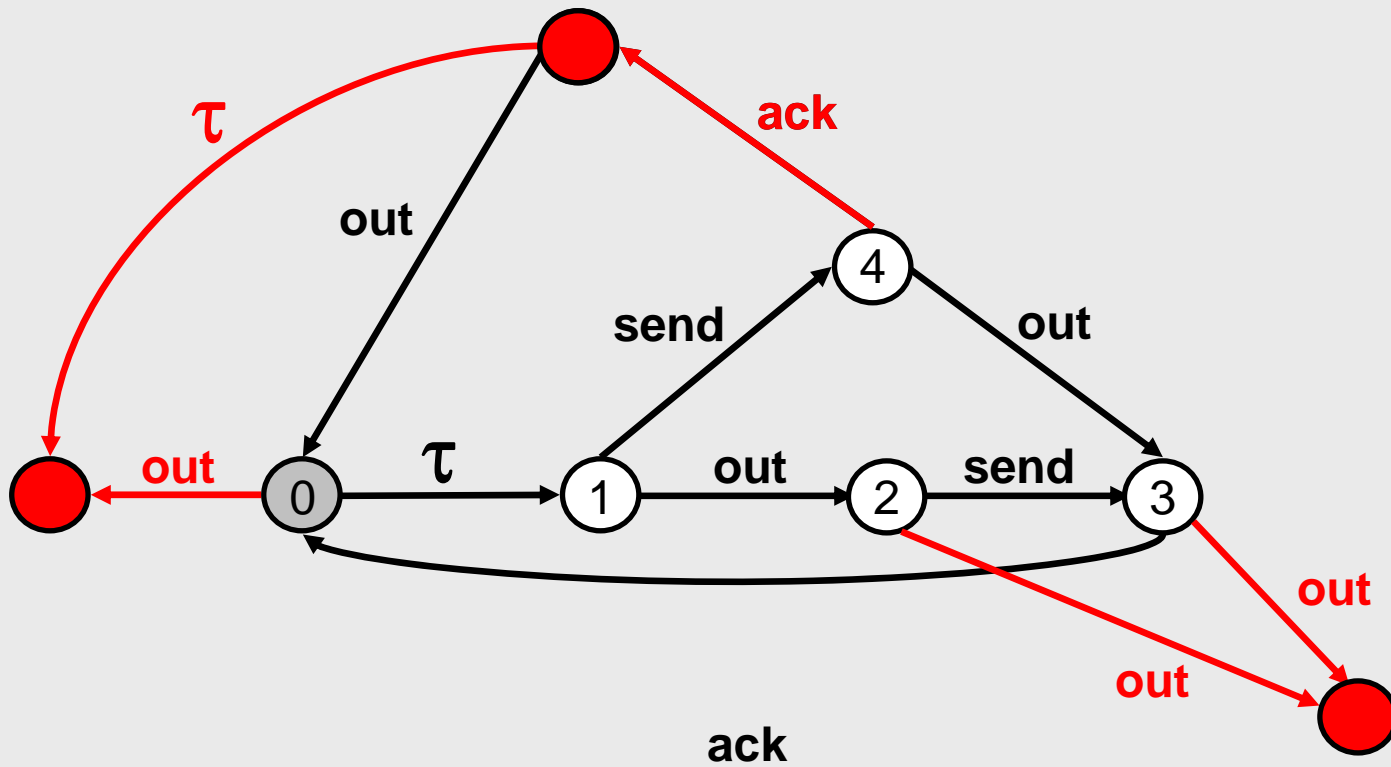


step I: composition & hiding

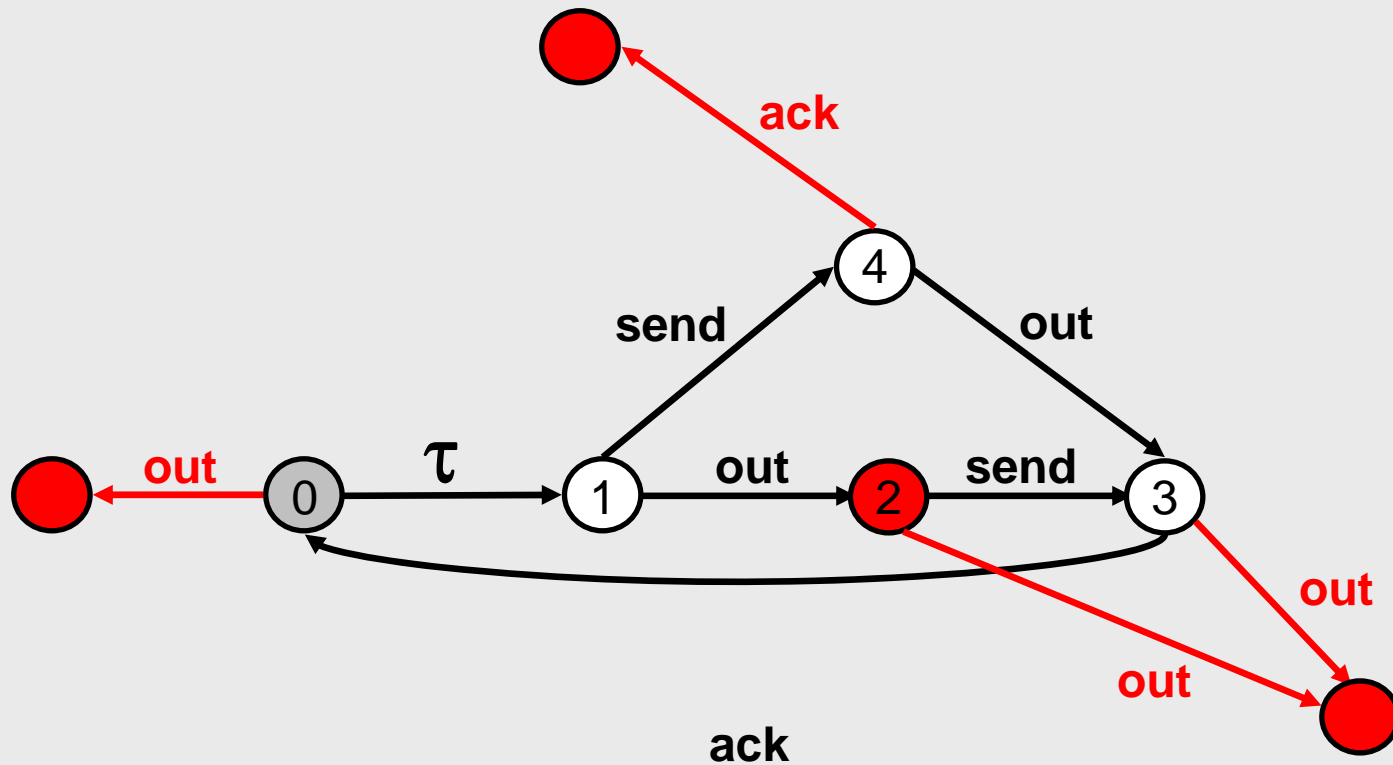
Input || Order_{err} \ {in}



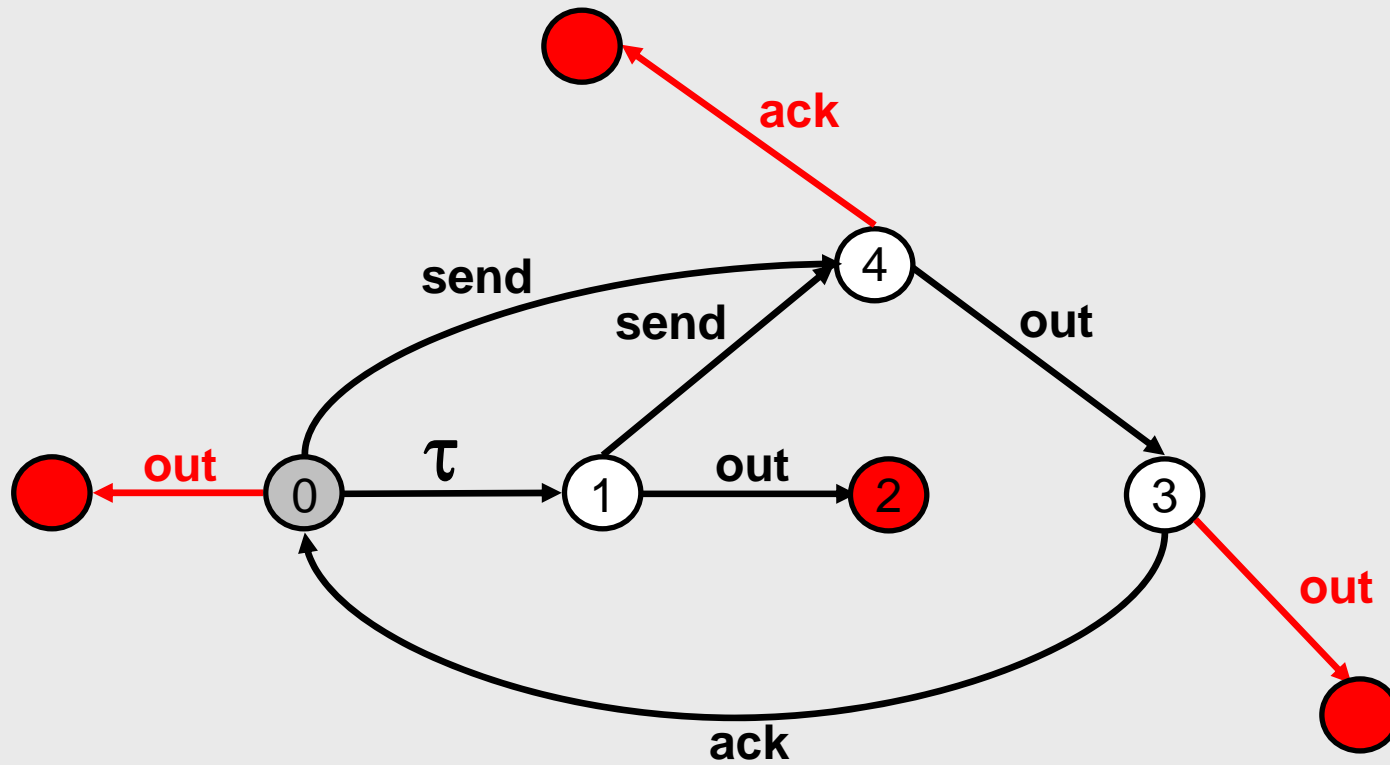
step 2: error propagation



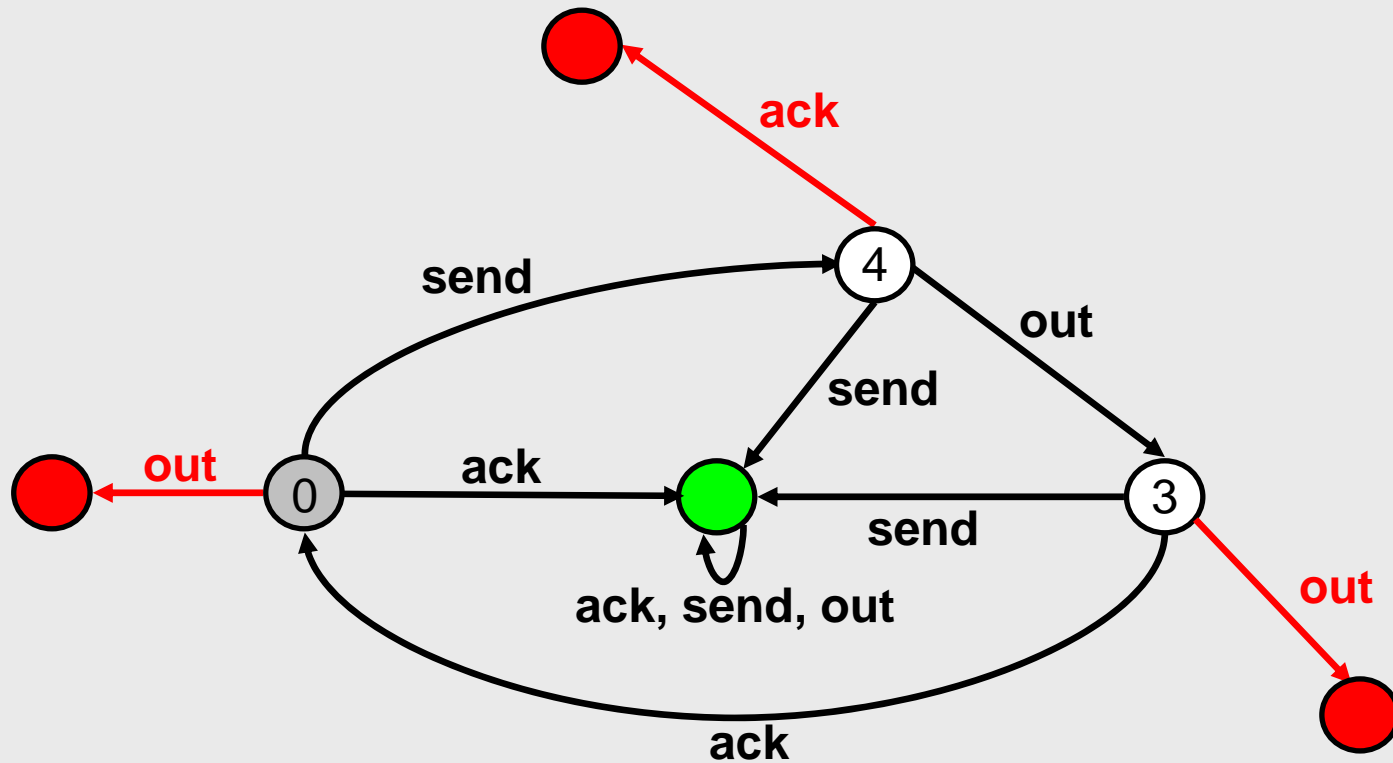
step 3: subset construction



step 3: subset construction



step 3: property construction



weakest assumption in AG reasoning

1. $\langle A \rangle M_1 \langle P \rangle$

2. $\langle true \rangle M_2 \langle A \rangle$

$\langle true \rangle M_1 \parallel M_2 \langle P \rangle$

weakest assumption makes
rule complete

$\langle WA \rangle M_1 \langle P \rangle$ holds (*WA* could be *false*)

$\langle true \rangle M_2 \langle WA \rangle$ holds implies $\langle true \rangle M_1 \parallel M_2 \langle P \rangle$ holds

$\langle true \rangle M_2 \langle WA \rangle$ not holds implies $\langle true \rangle M_1 \parallel M_2 \langle P \rangle$ not holds

iterative solution +
intermediate results

L^* learns unknown regular language
 U (over alphabet Σ) and produces
minimal DFA A such that $L(A) = U$
(L^* originally proposed by Angluin)

L* learner

the oracle

(queries)

should word w be included in $L(A)$?

yes / no

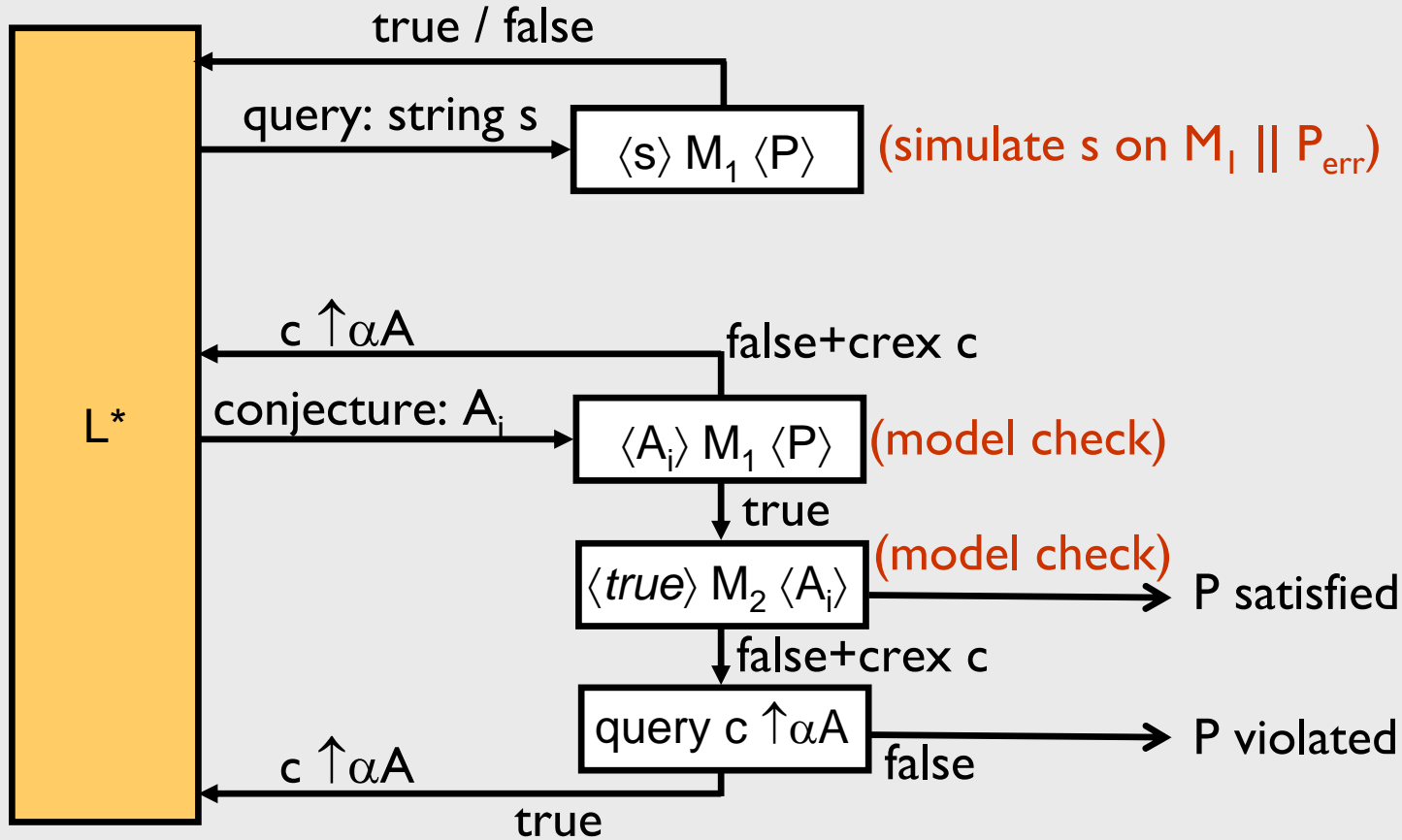
(conjectures)

here is an A – is $L(A) = U$?

yes!

no: word w should (not) be in $L(A)$

oracle for WA in assume-guarantee reasoning



$\langle WA \rangle M_1 \langle P \rangle$ holds (WA could be *false*)

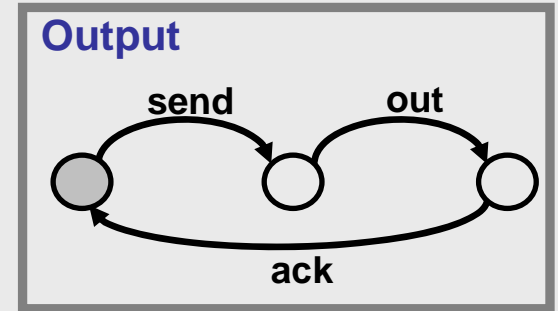
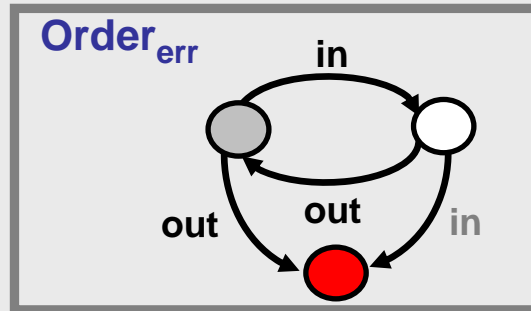
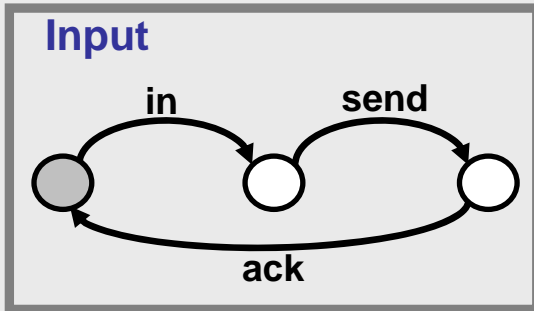
$\langle true \rangle M_2 \langle WA \rangle$ holds implies $\langle true \rangle M_1 \parallel M_2 \langle P \rangle$ holds

$\langle true \rangle M_2 \langle WA \rangle$ does not hold implies $\langle true \rangle M_1 \parallel M_2 \langle P \rangle$ does not hold

characteristics

- ▶ terminates with *minimal* automaton A for U
- ▶ generates DFA candidates A_i : $|A_1| < |A_2| < \dots < |A|$
- ▶ produces at most n candidates, where $n = |A|$
- ▶ # queries: $O(kn^2 + n \log m)$,
 - m is size of largest counterexample, k is size of alphabet
- ▶ for assume-guarantee reasoning, may terminate early with a smaller assumption than the weakest

example



we check: $\langle \text{true} \rangle \text{Input} \parallel \text{Output} \langle \text{Order} \rangle$

$M_1 = \text{Input}$, $M_2 = \text{Output}$, $P = \text{Order}$

assumption alphabet: $\{\text{send}, \text{out}, \text{ack}\}$

queries

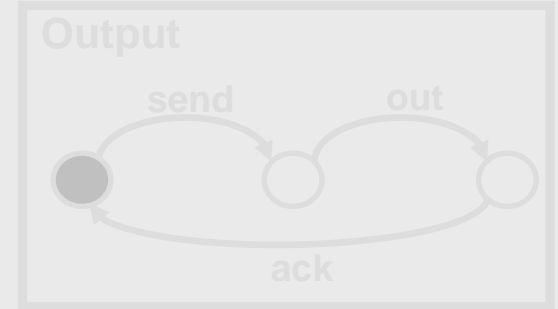
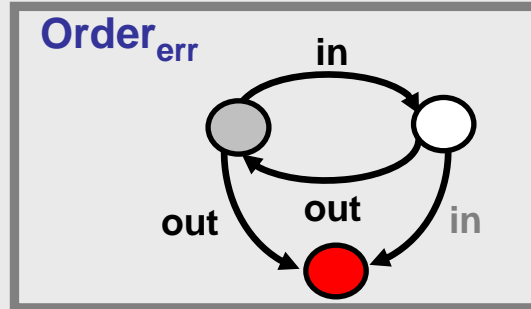
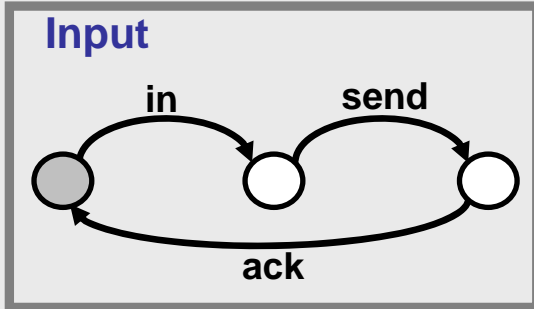


	Table T	E
	λ	λ
S	λ	true
	out	false
S · Σ	ack	true
	out	false
	send	true
	out, ack	false
	out, out	false
	out, send	false

closed (adds to **S**)
consistent (adds to **E**)

S = set of prefixes
E = set of suffixes

candidate construction

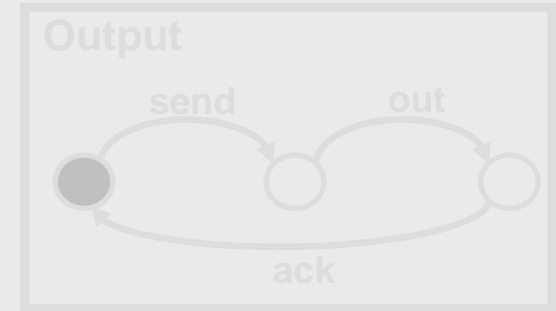
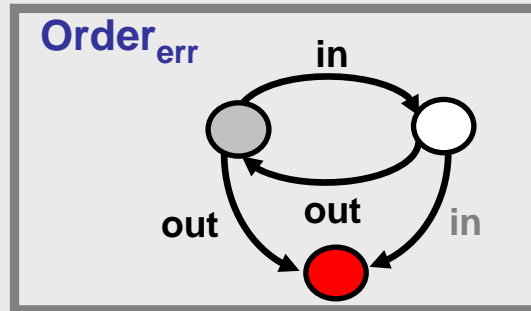
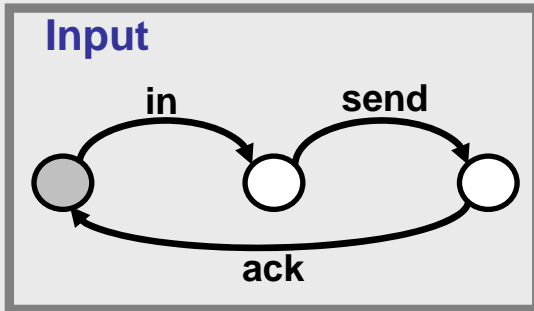


	Table T	E
	λ	λ
S	λ	true
	out	false
S · Σ	ack	true
	out	false
	send	true
	out, ack	false
	out, out	false
	out, send	false

2 states – error state omitted

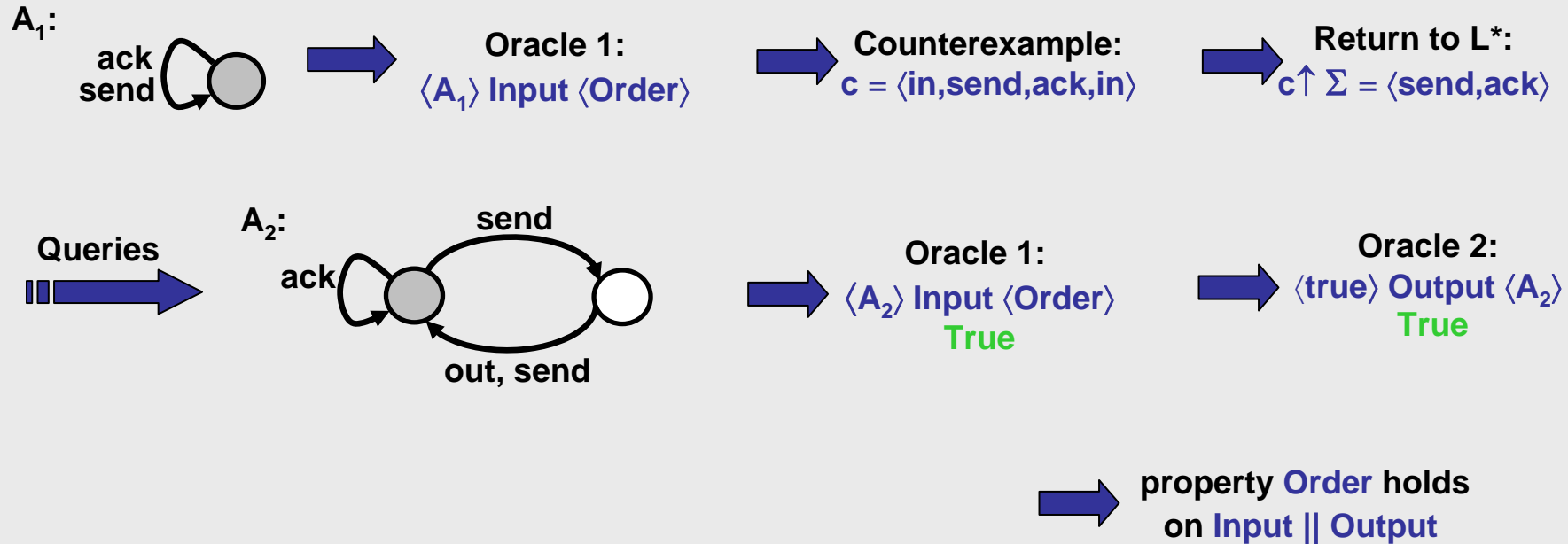
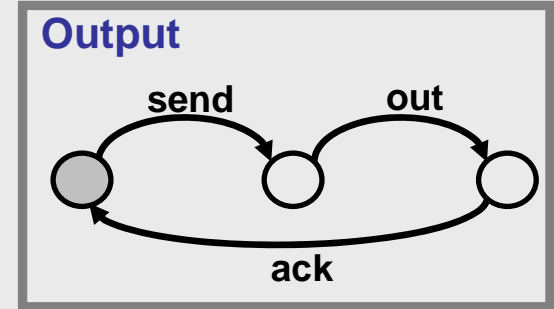
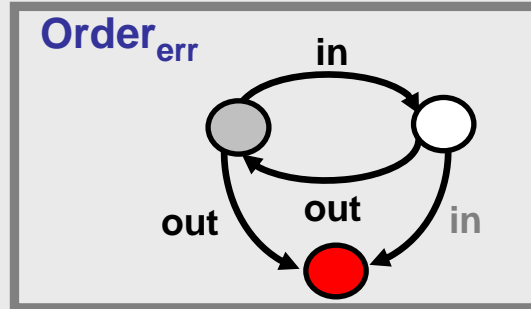
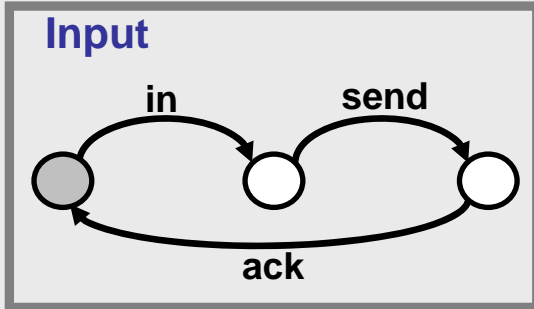
Assumption A₁



counterexamples add to S

S = set of prefixes
E = set of suffixes

conjectures



end of part I

please ask **LOTS** of questions!